

REPLACEMENT SHEET

Appln. Filing Date: September 3, 1999

Title: MOS TRANSISTOR WITH A CONTROLLED THRESHOLD VOLTAGE

Inventor(s): Toshiro Hiramoto et al.

Appln. No.: 09/389,321

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FIG. 1

PRIOR ART

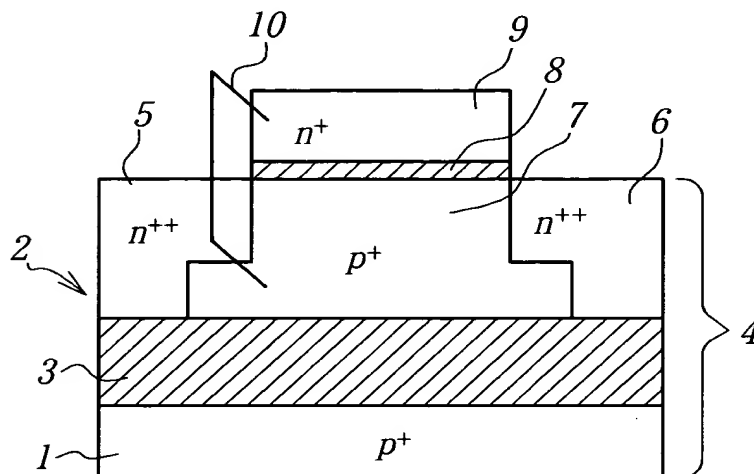
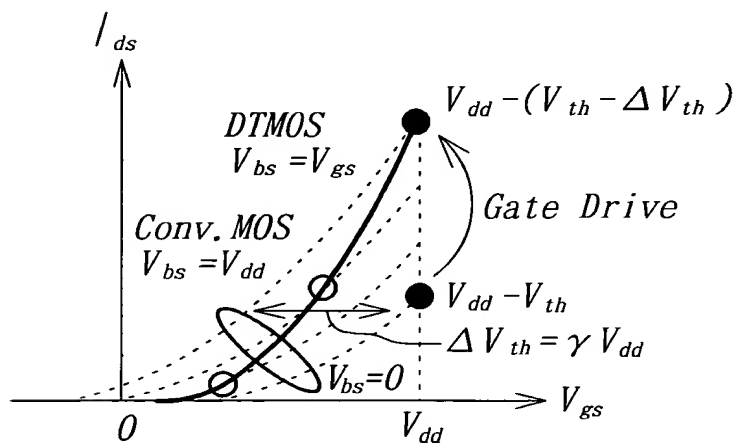


FIG. 2



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FIG. 3

PRIOR ART

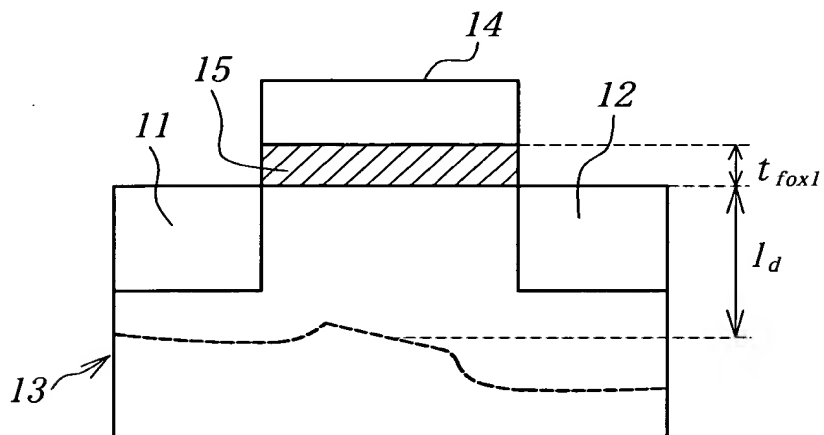


FIG. 4

PRIOR ART

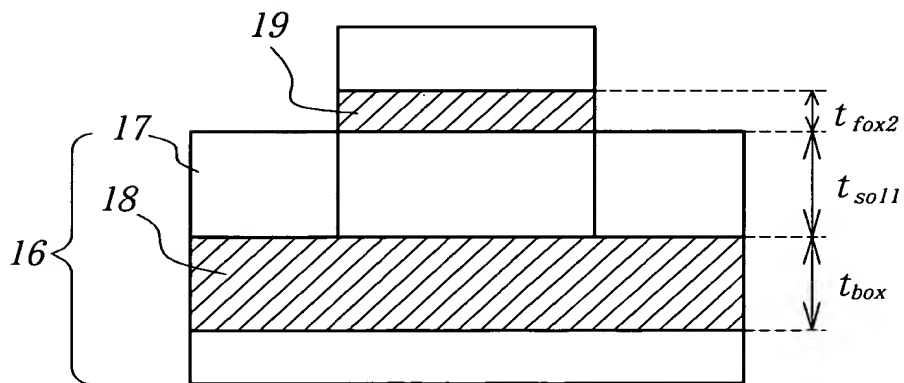




FIG. 5

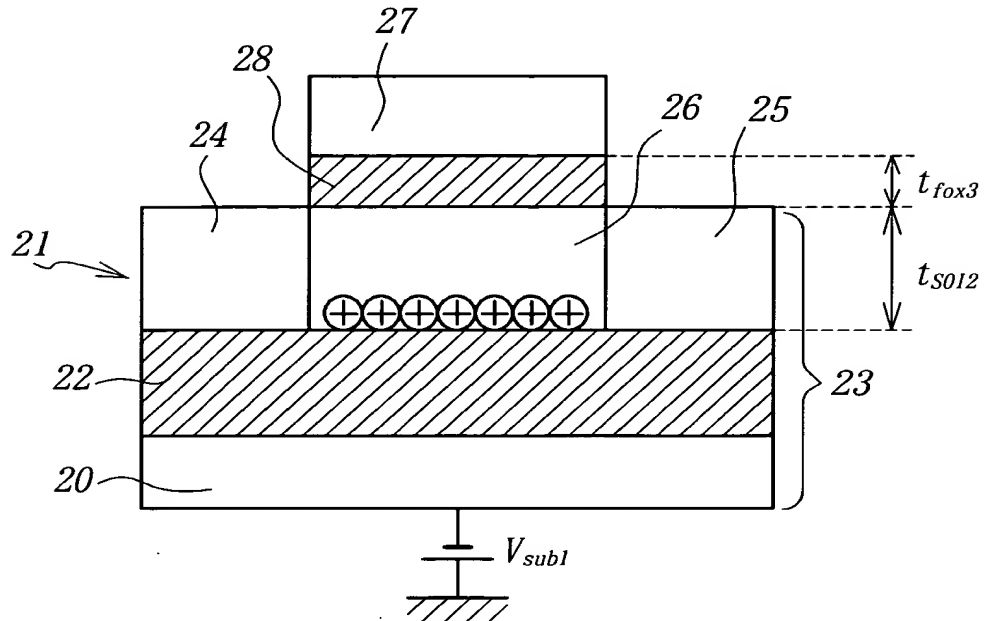


FIG. 6

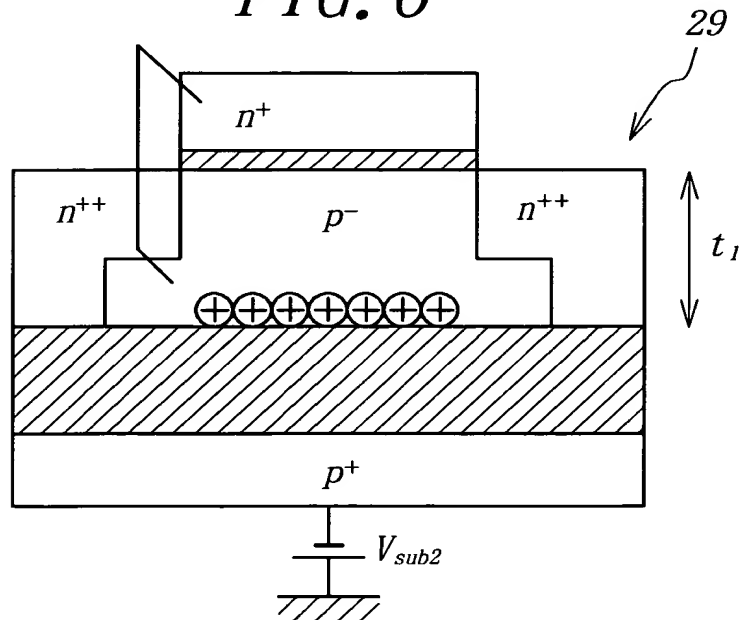




FIG. 7

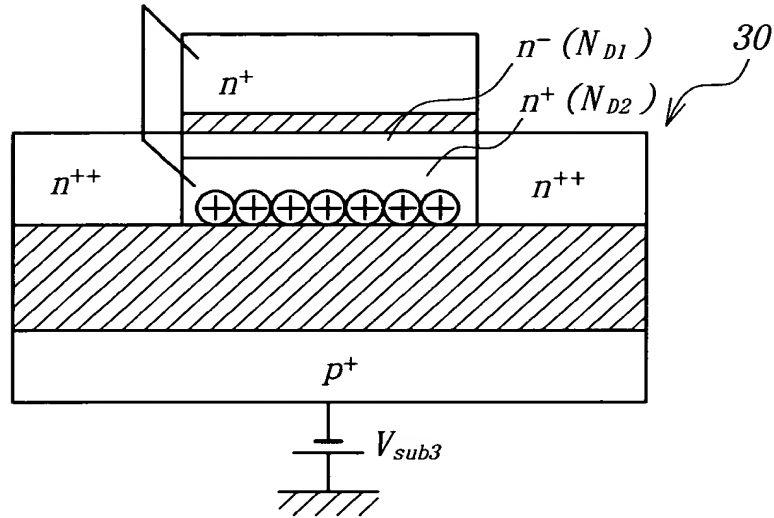
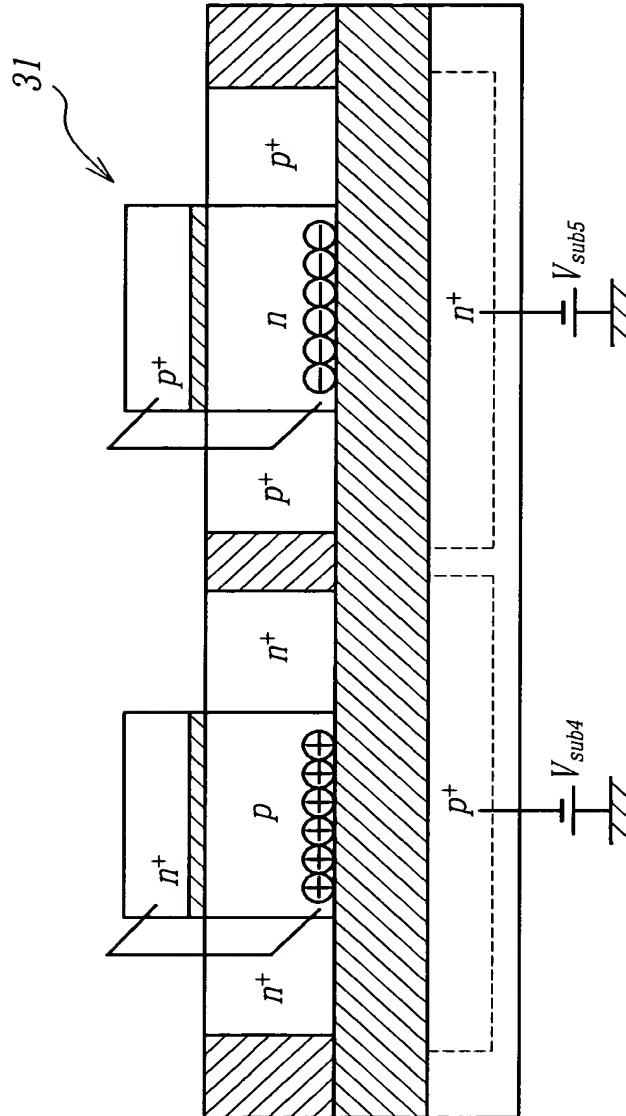




FIG. 8



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FIG. 9

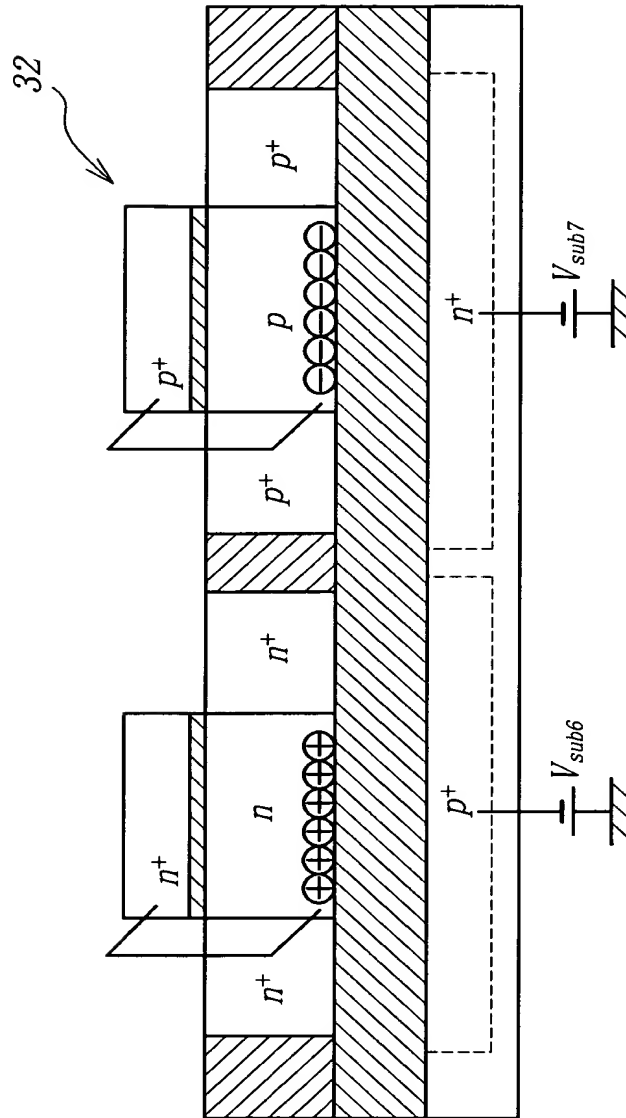




FIG. 10

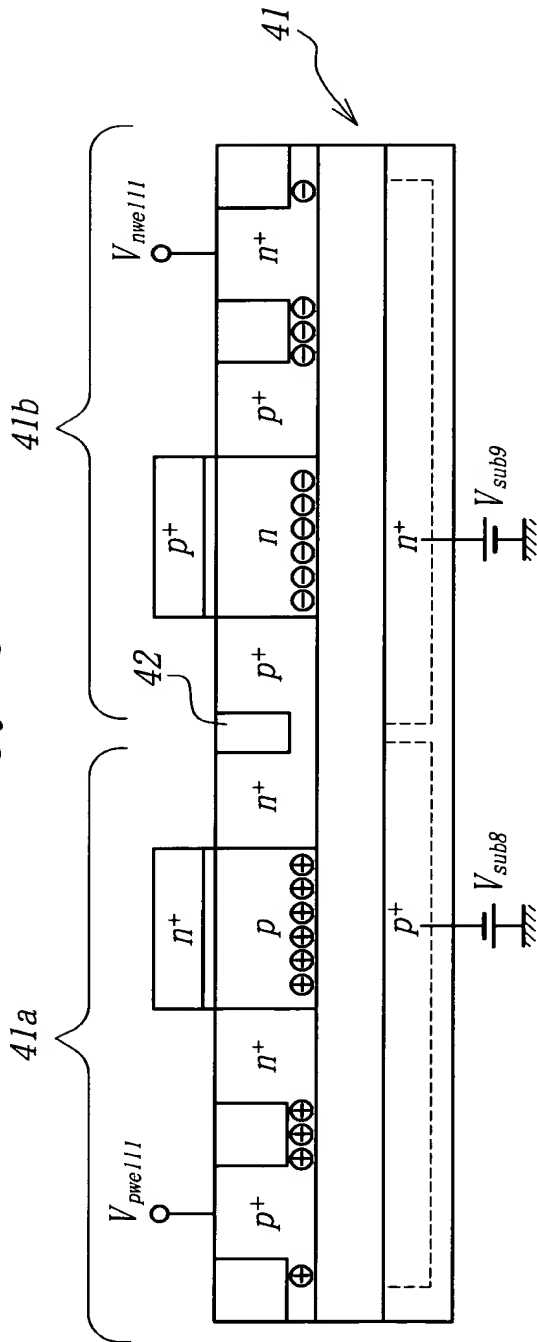
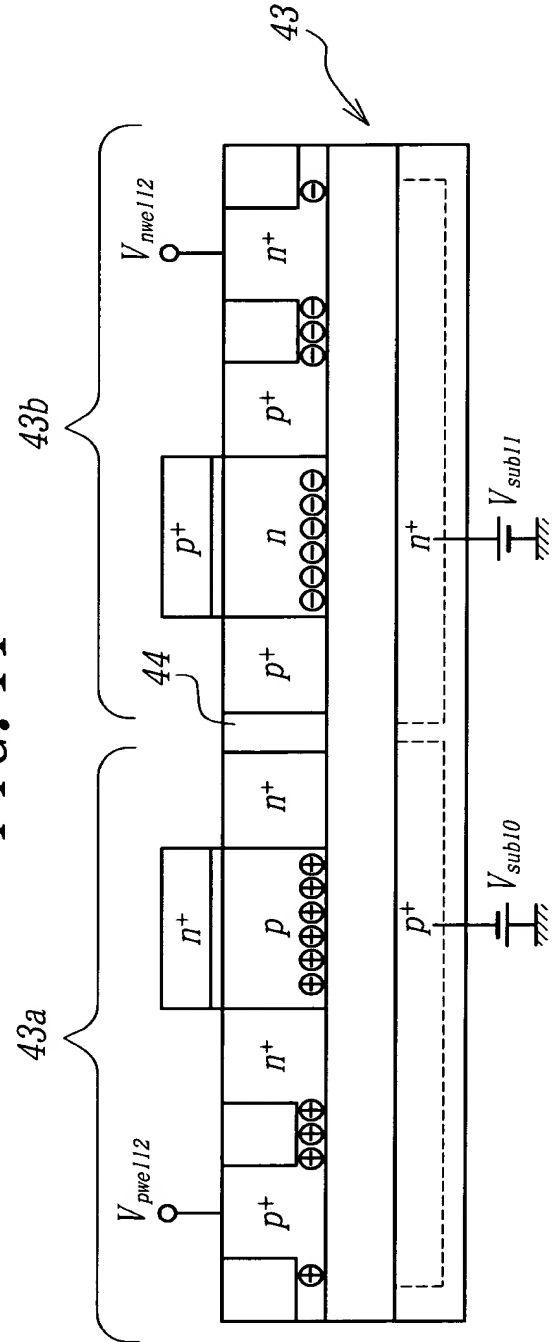


FIG. 11



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FIG. 12

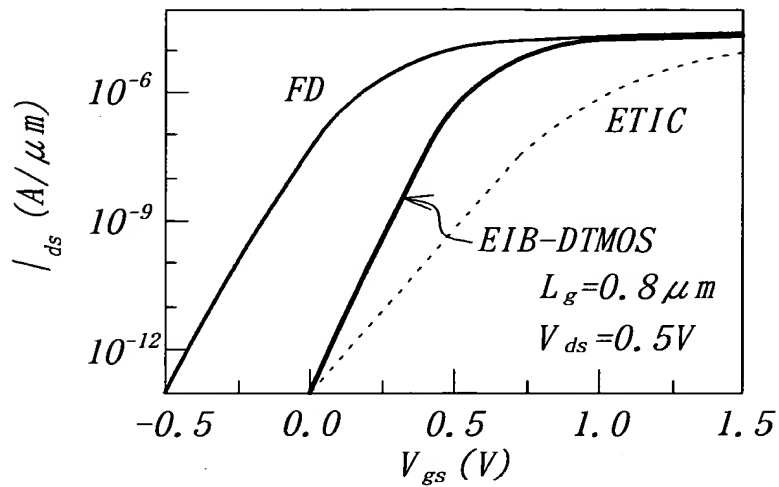


FIG. 13

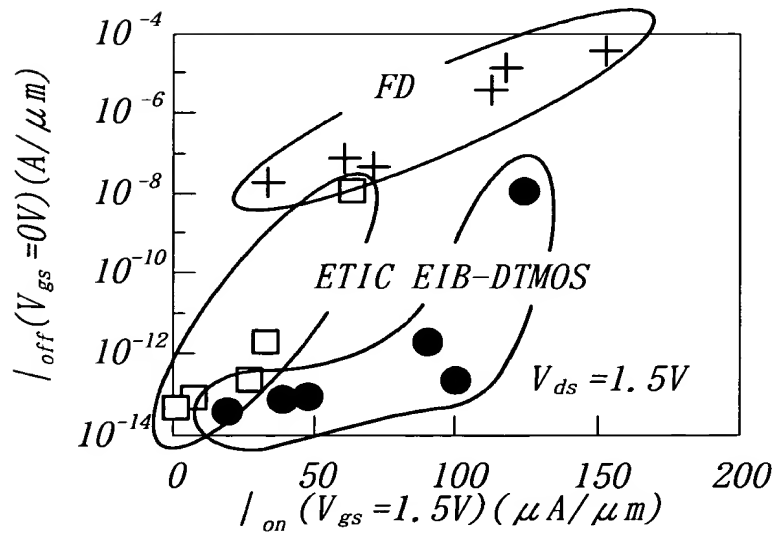




FIG. 14

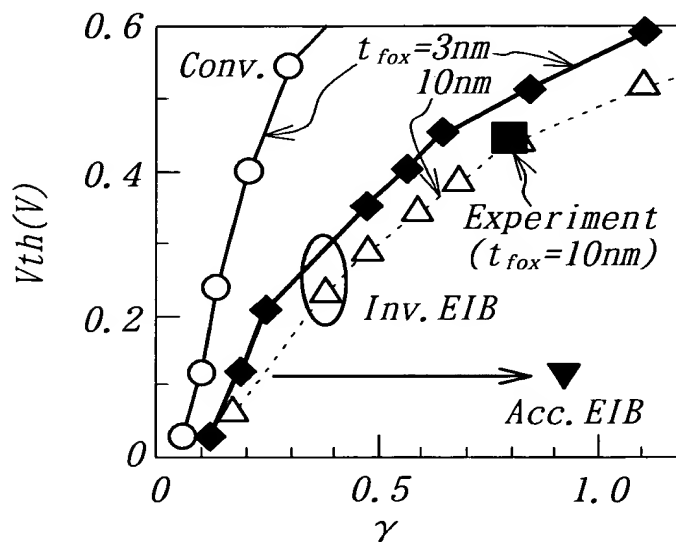


FIG. 15

